IN THE SPECIFICATION:

(1) The first and second paragraphs on page 3 of the specification are modified to read as follows:

"input terminal of the amplifier being connected to a reset circuit, wherein after accumulation of a light signal of the photoelectric converter, a reference signal held at the input terminal of the amplifier is read from the output terminal of the amplifier, the charge transfer circuit is ~~opened~~ turned on to transfer light signal charge of the photoelectric converter to the input terminal of the amplifier, after the charge transfer circuit is ~~closed~~ turned off, a light signal held at the input terminal of the amplifier is read from the output terminal of the amplifier as a light signal, the charge transfer circuit and the reset circuit are ~~opened~~ turned on to reset the output terminal of the photoelectric converter and the input terminal of the amplifier, and after the reset circuit is ~~closed~~turned off, the charge transfer circuit is ~~closed~~turned off, whereby subsequent accumulation of a light signal is conducted.

Alternatively, a photoelectric converter includes charge transfer circuit between an output terminal of photoelectric converter and an input terminal of amplifier, the output terminal of the photoelectric converter being connected to a reset circuit, wherein after accumulation of a light signal of the photoelectric converter, a reference signal held at the input terminal of the amplifier is read from the output terminal of the amplifier, the charge transfer circuit is ~~opened~~ turned on to transfer light signal charge of the photoelectric converter to the input terminal of the amplifier, after the charge transfer circuit is ~~closed~~turned off, a light signal held at the input terminal of the amplifier is read from the output terminal of the amplifier as a light signal, the charge transfer circuit and the reset circuit are ~~opened~~ turned on to reset the output terminal of the photoelectric converter and the input terminal of the amplifier, and after the reset means is ~~closed~~turned off, the charge transfer circuit is ~~closed~~turned off, whereby subsequent accumulation of a light signal is conducted.".

(2) The first paragraph on page 4 of the specification is modified to read as follows:

"holding circuit is connected to an input terminal of the second amplifier through second light signal transfer circuit, during a signal reading period, the second light signal transfer circuit is ~~opened~~ turned onto transfer the light signal held at the light signal holding circuit to the input terminal of the second amplifier, a light signal output is read from an output terminal of the second amplifier, after the second light signal transfer circuit is ~~closed~~ turned off or at the same time when the second light signal transfer circuit is ~~closed~~turned off, the second reference signal transfer circuit is ~~opened~~ turned on to transfer the reference signal held at the reference signal holding circuit to the input terminal of the second amplifier, and a reference signal output is read from the output terminal of the second amplifier.".

(3) The second paragraph on page 6 of the specification is modified to read as follows:

"Then, during a period REF1, a reference voltage corresponding to the potential of a terminal Vn is output as a reference output from a terminal Vo. When ϕT is turned on, the transfer switch 5 is ~~opened~~turned on, and charge accumulated in the N-region of the diode is transferred to the terminal Vn. As a result, potentials at a terminal Vdi and the terminal Vn become equal to each other. Next, ϕT is turned off, and the terminal Vn is supplied with a potential containing OFF noise thereof. During a period SIG1, a signal voltage corresponding to the potential at the terminal Vn (now equal to the transferred voltage from Vdi) is output as a signal output from the terminal Vo.".